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# Red Hat Enterprise Linux Release 9.2 Manual Pages on 'perf\_event\_open.2' command

# \$ man perf\_event\_open.2

PERF\_EVENT\_OPEN(2) Linux Programmer's Manual

PERF\_EVENT\_OPEN(2)

# NAME

perf\_event\_open - set up performance monitoring

# SYNOPSIS

#include <linux/perf\_event.h>

#include <linux/hw\_breakpoint.h>

int perf\_event\_open(struct perf\_event\_attr \*attr,

pid\_t pid, int cpu, int group\_fd,

unsigned long flags);

Note: There is no glibc wrapper for this system call; see NOTES.

## DESCRIPTION

Given a list of parameters, perf\_event\_open() returns a file descrip?

tor, for use in subsequent system calls (read(2), mmap(2), prctl(2),

fcntl(2), etc.).

A call to perf\_event\_open() creates a file descriptor that allows mea? suring performance information. Each file descriptor corresponds to one event that is measured; these can be grouped together to measure multiple events simultaneously.

Events can be enabled and disabled in two ways: via ioctl(2) and via prctl(2). When an event is disabled it does not count or generate overflows but does continue to exist and maintain its count value. Events come in two flavors: counting and sampled. A counting event is one that is used for counting the aggregate number of events that oc? cur. In general, counting event results are gathered with a read(2)

call. A sampling event periodically writes measurements to a buffer

that can then be accessed via mmap(2).

#### Arguments

The pid and cpu arguments allow specifying which process and CPU to monitor:

pid == 0 and cpu == -1

This measures the calling process/thread on any CPU.

```
pid == 0 and cpu \geq 0
```

This measures the calling process/thread only when running on

the specified CPU.

```
pid > 0 and cpu == -1
```

This measures the specified process/thread on any CPU.

```
pid > 0 and cpu >= 0
```

This measures the specified process/thread only when running on

the specified CPU.

pid == -1 and cpu >= 0

This measures all processes/threads on the specified CPU. This

requires CAP\_PERFMON (since Linux 5.8) or CAP\_SYS\_ADMIN capabil?

ity or a /proc/sys/kernel/perf\_event\_paranoid value of less than

```
1.
```

```
pid == -1 and cpu == -1
```

This setting is invalid and will return an error.

When pid is greater than zero, permission to perform this system call is governed by CAP\_PERFMON (since Linux 5.9) and a ptrace access mode PTRACE\_MODE\_READ\_REALCREDS check on older Linux versions; see ptrace(2).

```
The group_fd argument allows event groups to be created. An event group has one event which is the group leader. The leader is created first, with group_fd = -1. The rest of the group members are created with subsequent perf_event_open() calls with group_fd being set to the file descriptor of the group leader. (A single event on its own is created with group_fd = -1 and is considered to be a group with only 1
```

member.) An event group is scheduled onto the CPU as a unit: it will be put onto the CPU only if all of the events in the group can be put onto the CPU. This means that the values of the member events can be meaningfully compared?added, divided (to get ratios), and so on?with each other, since they have counted events for the same set of executed instructions.

The flags argument is formed by ORing together zero or more of the fol? lowing values:

PERF\_FLAG\_FD\_CLOEXEC (since Linux 3.14)

This flag enables the close-on-exec flag for the created event file descriptor, so that the file descriptor is automatically closed on execve(2). Setting the close-on-exec flags at cre? ation time, rather than later with fcntl(2), avoids potential race conditions where the calling thread invokes perf\_event\_open() and fcntl(2) at the same time as another thread calls fork(2) then execve(2).

#### PERF\_FLAG\_FD\_NO\_GROUP

This flag tells the event to ignore the group\_fd parameter ex? cept for the purpose of setting up output redirection using the PERF\_FLAG\_FD\_OUTPUT flag.

PERF\_FLAG\_FD\_OUTPUT (broken since Linux 2.6.35)

This flag re-routes the event's sampled output to instead be in? cluded in the mmap buffer of the event specified by group\_fd.

PERF\_FLAG\_PID\_CGROUP (since Linux 2.6.39)

This flag activates per-container system-wide monitoring. A container is an abstraction that isolates a set of resources for finer-grained control (CPUs, memory, etc.). In this mode, the event is measured only if the thread running on the monitored CPU belongs to the designated container (cgroup). The cgroup is identified by passing a file descriptor opened on its directory in the cgroupfs filesystem. For instance, if the cgroup to mon? itor is called test, then a file descriptor opened on /dev/cgroup/test (assuming cgroupfs is mounted on /dev/cgroup)

must be passed as the pid parameter. cgroup monitoring is available only for system-wide events and may therefore require extra permissions.

The perf\_event\_attr structure provides detailed configuration informa? tion for the event being created.

struct perf\_event\_attr {

u32 type;	/* Type of event */
u32 size;	/* Size of attribute structure */
u64 config;	/* Type-specific configuration */
union {	
u64 sample_p	period; /* Period of sampling */
u64 sample_f	req; /* Frequency of sampling */
};	
u64 sample_type	e; /* Specifies values included in sample */
u64 read_format	; /* Specifies values returned in read */
u64 disabled	: 1, /* off by default */
inherit : 1,	/* children inherit it */
pinned : 1	, /* must always be on PMU */
exclusive : 1	l, /* only group on PMU */
exclude_user	: 1, /* don't count user */
exclude_kernel	: 1, /* don't count kernel */
exclude_hv :	1, /* don't count hypervisor */
exclude_idle :	1, /* don't count when idle */
mmap : '	I, /* include mmap data */
comm : 1	, /* include comm data */
freq : 1,	/* use freq, not period */
inherit_stat : 1	, /* per task counts */
enable_on_exe	ec : 1, /* next exec enables */
task : 1,	/* trace fork/exit */
watermark :	1, /* wakeup_watermark */
precise_ip : 2	2, /* skid constraint */
mmap_data	: 1, /* non-exec mmap data */

sample\_id\_all : 1, /\* sample\_type all events \*/

exclude\_host : 1, /\* don't count in host \*/

exclude\_guest : 1, /\* don't count in guest \*/

exclude\_callchain\_kernel: 1,

/\* exclude kernel callchains \*/

exclude\_callchain\_user : 1,

/\* exclude user callchains \*/

- mmap2 : 1, /\* include mmap with inode data \*/
- comm\_exec : 1, /\* flag comm events that are due to exec \*/
- use\_clockid : 1, /\* use clockid for time fields \*/
- context\_switch : 1, /\* context switch data \*/
- write\_backward : 1, /\* Write ring buffer from end to beginning \*/
- namespaces : 1, /\* include namespaces data \*/
- ksymbol : 1, /\* include ksymbol events \*/
- bpf\_event : 1, /\* include bpf events \*/
- aux\_output : 1, /\* generate AUX records instead of events \*/
- cgroup : 1, /\* include cgroup events \*/
- text\_poke : 1, /\* include text poke events \*/

\_\_reserved\_1 : 30;

## union {

```
__u32 wakeup_events; /* wakeup every n events */
__u32 wakeup_watermark; /* bytes before wakeup */
```

```
};
```

\_\_u32 bp\_type; /\* breakpoint type \*/

union {

\_\_u64 bp\_addr; /\* breakpoint address \*/
\_\_u64 kprobe\_func; /\* for perf\_kprobe \*/
\_\_u64 uprobe\_path; /\* for perf\_uprobe \*/
\_\_u64 config1; /\* extension of config \*/

```
};
```

```
/* breakpoint length */
   u64 bp len;
  ___u64 kprobe_addr;
                      /* with kprobe func == NULL */
  __u64 probe_offset; /* for perf_[k,u]probe */
                     /* extension of config1 */
  __u64 config2;
};
__u64 branch_sample_type; /* enum perf_branch_sample_type */
__u64 sample_regs_user; /* user regs to dump on samples */
__u32 sample_stack_user; /* size of stack to dump on
                  samples */
s32 clockid;
                     /* clock to use for time fields */
__u64 sample_regs_intr; /* regs to dump on samples */
                         /* aux bytes before wakeup */
__u32 aux_watermark;
___u16 sample_max_stack; /* max frames in callchain */
__u16 __reserved_2; /* align to u64 */
```

```
};
```

The fields of the perf\_event\_attr structure are described in more de? tail below:

type This field specifies the overall event type. It has one of the

following values:

## PERF\_TYPE\_HARDWARE

This indicates one of the "generalized" hardware events

provided by the kernel. See the config field definition

for more details.

# PERF\_TYPE\_SOFTWARE

This indicates one of the software-defined events pro?

vided by the kernel (even if no hardware support is

available).

# PERF\_TYPE\_TRACEPOINT

This indicates a tracepoint provided by the kernel trace?

point infrastructure.

## PERF\_TYPE\_HW\_CACHE

This indicates a hardware cache event. This has a spe?

cial encoding, described in the config field definition.

#### PERF\_TYPE\_RAW

This indicates a "raw" implementation-specific event in the config field.

PERF\_TYPE\_BREAKPOINT (since Linux 2.6.33)

This indicates a hardware breakpoint as provided by the CPU. Breakpoints can be read/write accesses to an ad? dress as well as execution of an instruction address.

dynamic PMU

Since Linux 2.6.38, perf\_event\_open() can support multi? ple PMUs. To enable this, a value exported by the kernel can be used in the type field to indicate which PMU to use. The value to use can be found in the sysfs filesys? tem: there is a subdirectory per PMU instance under /sys/bus/event\_source/devices. In each subdirectory there is a type file whose content is an integer that can be used in the type field. For instance, /sys/bus/event\_source/devices/cpu/type contains the value for the core CPU PMU, which is usually 4.

kprobe and uprobe (since Linux 4.17)

These two dynamic PMUs create a kprobe/uprobe and attach it to the file descriptor generated by perf\_event\_open. The kprobe/uprobe will be destroyed on the destruction of the file descriptor. See fields kprobe\_func, up? robe\_path, kprobe\_addr, and probe\_offset for more de? tails.

size The size of the perf\_event\_attr structure for forward/backward compatibility. Set this using sizeof(struct perf\_event\_attr) to allow the kernel to see the struct size at the time of compila? tion.

The related define PERF\_ATTR\_SIZE\_VER0 is set to 64; this was the size of the first published struct. PERF\_ATTR\_SIZE\_VER1 is 72, corresponding to the addition of breakpoints in Linux 2.6.33. PERF\_ATTR\_SIZE\_VER2 is 80 corresponding to the addition of branch sampling in Linux 3.4. PERF\_ATTR\_SIZE\_VER3 is 96 cor? responding to the addition of sample\_regs\_user and sam? ple\_stack\_user in Linux 3.7. PERF\_ATTR\_SIZE\_VER4 is 104 corre? sponding to the addition of sample\_regs\_intr in Linux 3.19. PERF\_ATTR\_SIZE\_VER5 is 112 corresponding to the addition of aux\_watermark in Linux 4.1.

config This specifies which event you want, in conjunction with the type field. The config1 and config2 fields are also taken into account in cases where 64 bits is not enough to fully specify the event. The encoding of these fields are event dependent. There are various ways to set the config field that are depen? dent on the value of the previously described type field. What follows are various possible settings for config separated out by type.

If type is PERF\_TYPE\_HARDWARE, we are measuring one of the gen? eralized hardware CPU events. Not all of these are available on all platforms. Set config to one of the following:

### PERF\_COUNT\_HW\_CPU\_CYCLES

Total cycles. Be wary of what happens during CPU frequency scaling.

#### PERF\_COUNT\_HW\_INSTRUCTIONS

Retired instructions. Be careful, these can be af? fected by various issues, most notably hardware in? terrupt counts.

## PERF\_COUNT\_HW\_CACHE\_REFERENCES

Cache accesses. Usually this indicates Last Level Cache accesses but this may vary depending on your CPU. This may include prefetches and coherency mes? sages; again this depends on the design of your CPU.

### PERF\_COUNT\_HW\_CACHE\_MISSES

Cache misses. Usually this indicates Last Level

Cache misses; this is intended to be used in con?

junction with the PERF\_COUNT\_HW\_CACHE\_REFERENCES

event to calculate cache miss rates.

### PERF\_COUNT\_HW\_BRANCH\_INSTRUCTIONS

Retired branch instructions. Prior to Linux 2.6.35,

this used the wrong event on AMD processors.

PERF\_COUNT\_HW\_BRANCH\_MISSES

Mispredicted branch instructions.

PERF\_COUNT\_HW\_BUS\_CYCLES

Bus cycles, which can be different from total cy?

cles.

PERF\_COUNT\_HW\_STALLED\_CYCLES\_FRONTEND (since Linux 3.0)

Stalled cycles during issue.

PERF\_COUNT\_HW\_STALLED\_CYCLES\_BACKEND (since Linux 3.0)

Stalled cycles during retirement.

PERF\_COUNT\_HW\_REF\_CPU\_CYCLES (since Linux 3.3)

Total cycles; not affected by CPU frequency scaling.

If type is PERF\_TYPE\_SOFTWARE, we are measuring software events

provided by the kernel. Set config to one of the following:

## PERF\_COUNT\_SW\_CPU\_CLOCK

This reports the CPU clock, a high-resolution per-

CPU timer.

## PERF\_COUNT\_SW\_TASK\_CLOCK

This reports a clock count specific to the task that

is running.

## PERF\_COUNT\_SW\_PAGE\_FAULTS

This reports the number of page faults.

## PERF\_COUNT\_SW\_CONTEXT\_SWITCHES

This counts context switches. Until Linux 2.6.34,

these were all reported as user-space events, after

that they are reported as happening in the kernel.

## PERF\_COUNT\_SW\_CPU\_MIGRATIONS

This reports the number of times the process has mi?

grated to a new CPU.

This counts the number of minor page faults. These

did not require disk I/O to handle.

## PERF\_COUNT\_SW\_PAGE\_FAULTS\_MAJ

This counts the number of major page faults. These required disk I/O to handle.

PERF\_COUNT\_SW\_ALIGNMENT\_FAULTS (since Linux 2.6.33)

This counts the number of alignment faults. These happen when unaligned memory accesses happen; the kernel can handle these but it reduces performance. This happens only on some architectures (never on x86).

## PERF\_COUNT\_SW\_EMULATION\_FAULTS (since Linux 2.6.33)

This counts the number of emulation faults. The kernel sometimes traps on unimplemented instructions and emulates them for user space. This can nega? tively impact performance.

# PERF\_COUNT\_SW\_DUMMY (since Linux 3.12)

This is a placeholder event that counts nothing. Informational sample record types such as mmap or comm must be associated with an active event. This dummy event allows gathering such records without requiring a counting event.

If type is PERF\_TYPE\_TRACEPOINT, then we are measuring kernel tracepoints. The value to use in config can be obtained from under debugfs tracing/events/\*/\*/id if ftrace is enabled in the kernel.

If type is  $\mathsf{PERF\_TYPE\_HW\_CACHE},$  then we are measuring a hardware

CPU cache event. To calculate the appropriate config value, use

the following equation:

```
config = (perf_hw_cache_id) |
```

```
(perf_hw_cache_op_id << 8) |
```

```
(perf_hw_cache_op_result_id << 16);
```

where perf\_hw\_cache\_id is one of:

## PERF\_COUNT\_HW\_CACHE\_L1D

for measuring Level 1 Data Cache

# PERF\_COUNT\_HW\_CACHE\_L1I

for measuring Level 1 Instruction Cache

# PERF\_COUNT\_HW\_CACHE\_LL

for measuring Last-Level Cache

# PERF\_COUNT\_HW\_CACHE\_DTLB

for measuring the Data TLB

# PERF\_COUNT\_HW\_CACHE\_ITLB

for measuring the Instruction TLB

# PERF\_COUNT\_HW\_CACHE\_BPU

for measuring the branch prediction unit

# PERF\_COUNT\_HW\_CACHE\_NODE (since Linux 3.1)

for measuring local memory accesses

# and perf\_hw\_cache\_op\_id is one of:

PERF\_COUNT\_HW\_CACHE\_OP\_READ

for read accesses

# PERF\_COUNT\_HW\_CACHE\_OP\_WRITE

for write accesses

# PERF\_COUNT\_HW\_CACHE\_OP\_PREFETCH

for prefetch accesses

and perf\_hw\_cache\_op\_result\_id is one of:

# PERF\_COUNT\_HW\_CACHE\_RESULT\_ACCESS

to measure accesses

# PERF\_COUNT\_HW\_CACHE\_RESULT\_MISS

to measure misses

If type is PERF_TYPE_RAW, then a custom "raw" config value is	
needed. Most CPUs support events that are not covered by the	
"generalized" events. These are implementation defined; see	
your CPU manual (for example the Intel Volume 3B documentation	
or the AMD BIOS and Kernel Developer Guide). The libpfm4 li?	
brary can be used to translate from the name in the architec?	
tural manuals to the raw hex value perf_event_open() expects in	

this field.

If type is PERF\_TYPE\_BREAKPOINT, then leave config set to zero. Its parameters are set in other places. If type is kprobe or uprobe, set retprobe (bit 0 of config, see /sys/bus/event\_source/devices/[k,u]probe/format/retprobe) for kretprobe/uretprobe. See fields kprobe\_func, uprobe\_path, kprobe\_addr, and probe\_offset for more details.

kprobe\_func, uprobe\_path, kprobe\_addr, and probe\_offset

These fields describe the kprobe/uprobe for dynamic PMUs kprobe and uprobe. For kprobe: use kprobe\_func and probe\_offset, or use kprobe\_addr and leave kprobe\_func as NULL. For uprobe: use uprobe\_path and probe\_offset.

sample\_period, sample\_freq

A "sampling" event is one that generates an overflow notifica? tion every N events, where N is given by sample\_period. A sam? pling event has sample\_period > 0. When an overflow occurs, re? quested data is recorded in the mmap buffer. The sample\_type field controls what data is recorded on each overflow. sample\_freq can be used if you wish to use frequency rather than period. In this case, you set the freq flag. The kernel will adjust the sampling period to try and achieve the desired rate. The rate of adjustment is a timer tick.

#### sample\_type

The various bits in this field specify which values to include in the sample. They will be recorded in a ring-buffer, which is available to user space using mmap(2). The order in which the values are saved in the sample are documented in the MMAP Layout subsection below; it is not the enum perf\_event\_sample\_format order.

#### PERF\_SAMPLE\_IP

Records instruction pointer.

PERF\_SAMPLE\_TID

Records the process and thread IDs.

### PERF\_SAMPLE\_TIME

Records a timestamp.

### PERF\_SAMPLE\_ADDR

Records an address, if applicable.

### PERF\_SAMPLE\_READ

Record counter values for all events in a group, not just

the group leader.

### PERF\_SAMPLE\_CALLCHAIN

Records the callchain (stack backtrace).

### PERF\_SAMPLE\_ID

Records a unique ID for the opened event's group leader.

### PERF\_SAMPLE\_CPU

Records CPU number.

### PERF\_SAMPLE\_PERIOD

Records the current sampling period.

### PERF\_SAMPLE\_STREAM\_ID

Records a unique ID for the opened event. Unlike

PERF\_SAMPLE\_ID the actual ID is returned, not the group

leader. This ID is the same as the one returned by

PERF\_FORMAT\_ID.

### PERF\_SAMPLE\_RAW

Records additional data, if applicable. Usually returned

by tracepoint events.

# PERF\_SAMPLE\_BRANCH\_STACK (since Linux 3.4)

This provides a record of recent branches, as provided by

CPU branch sampling hardware (such as Intel Last Branch

Record). Not all hardware supports this feature.

See the branch\_sample\_type field for how to filter which

branches are reported.

## PERF\_SAMPLE\_REGS\_USER (since Linux 3.7)

Records the current user-level CPU register state (the values in the process before the kernel was called).

PERF\_SAMPLE\_STACK\_USER (since Linux 3.7)

Records the user level stack, allowing stack unwinding.

PERF\_SAMPLE\_WEIGHT (since Linux 3.10) Records a hardware provided weight value that expresses how costly the sampled event was. This allows the hard? ware to highlight expensive events in a profile.

PERF\_SAMPLE\_DATA\_SRC (since Linux 3.10)

Records the data source: where in the memory hierarchy the data associated with the sampled instruction came from. This is available only if the underlying hardware supports this feature.

PERF\_SAMPLE\_IDENTIFIER (since Linux 3.12)

Places the SAMPLE\_ID value in a fixed position in the record, either at the beginning (for sample events) or at the end (if a non-sample event).

This was necessary because a sample stream may have records from various different event sources with differ? ent sample\_type settings. Parsing the event stream prop? erly was not possible because the format of the record was needed to find SAMPLE\_ID, but the format could not be found without knowing what event the sample belonged to (causing a circular dependency).

The PERF\_SAMPLE\_IDENTIFIER setting makes the event stream always parsable by putting SAMPLE\_ID in a fixed location, even though it means having duplicate SAMPLE\_ID values in records.

PERF\_SAMPLE\_TRANSACTION (since Linux 3.13)

Records reasons for transactional memory abort events (for example, from Intel TSX transactional memory sup? port).

The precise\_ip setting must be greater than 0 and a transactional memory abort event must be measured or no values will be recorded. Also note that some perf\_event measurements, such as sampled cycle counting, may cause

extraneous aborts (by causing an interrupt during a transaction).

PERF\_SAMPLE\_REGS\_INTR (since Linux 3.19) Records a subset of the current CPU register state as specified by sample\_regs\_intr. Unlike PERF\_SAM? PLE\_REGS\_USER the register values will return kernel reg? ister state if the overflow happened while kernel code is running. If the CPU supports hardware sampling of regis? ter state (i.e., PEBS on Intel x86) and precise\_ip is set higher than zero then the register values returned are those captured by hardware at the time of the sampled in? struction's retirement.

PERF\_SAMPLE\_PHYS\_ADDR (since Linux 4.13) Records physical address of data like in PERF\_SAM? PLE\_ADDR.

PERF\_SAMPLE\_CGROUP (since Linux 5.7)

Records (perf\_event) cgroup ID of the process. This cor?

responds to the id field in the PERF\_RECORD\_CGROUP event.

### read\_format

This field specifies the format of the data returned by read(2)

on a perf\_event\_open() file descriptor.

#### PERF\_FORMAT\_TOTAL\_TIME\_ENABLED

Adds the 64-bit time\_enabled field. This can be used to calculate estimated totals if the PMU is overcommitted and multiplexing is happening.

### PERF\_FORMAT\_TOTAL\_TIME\_RUNNING

Adds the 64-bit time\_running field. This can be used to calculate estimated totals if the PMU is overcommitted and multiplexing is happening.

#### PERF\_FORMAT\_ID

Adds a 64-bit unique value that corresponds to the event

group.

Allows all counter values in an event group to be read with one read.

#### disabled

The disabled bit specifies whether the counter starts out dis? abled or enabled. If disabled, the event can later be enabled by ioctl(2), prctl(2), or enable\_on\_exec.

When creating an event group, typically the group leader is ini? tialized with disabled set to 1 and any child events are ini? tialized with disabled set to 0. Despite disabled being 0, the child events will not start until the group leader is enabled.

#### inherit

The inherit bit specifies that this counter should count events of child tasks as well as the task specified. This applies only to new children, not to any existing children at the time the counter is created (nor to any new children of existing chil? dren).

Inherit does not work for some combinations of read\_format val? ues, such as PERF\_FORMAT\_GROUP.

pinned The pinned bit specifies that the counter should always be on the CPU if at all possible. It applies only to hardware coun? ters and only to group leaders. If a pinned counter cannot be put onto the CPU (e.g., because there are not enough hardware counters or because of a conflict with some other event), then the counter goes into an 'error' state, where reads return endof-file (i.e., read(2) returns 0) until the counter is subse? quently enabled or disabled.

#### exclusive

The exclusive bit specifies that when this counter's group is on the CPU, it should be the only group using the CPU's counters. In the future this may allow monitoring programs to support PMU features that need to run alone so that they do not disrupt other hardware counters.

Note that many unexpected situations may prevent events with the

exclusive bit set from ever running. This includes any users running a system-wide measurement as well as any kernel use of the performance counters (including the commonly enabled NMI Watchdog Timer interface).

#### exclude\_user

If this bit is set, the count excludes events that happen in user space.

#### exclude\_kernel

If this bit is set, the count excludes events that happen in kernel space.

#### exclude\_hv

If this bit is set, the count excludes events that happen in the hypervisor. This is mainly for PMUs that have built-in support for handling this (such as POWER). Extra support is needed for handling hypervisor measurements on most machines.

#### exclude\_idle

If set, don't count when the CPU is running the idle task.

While you can currently enable this for any event type, it is

ignored for all but software events.

mmap The mmap bit enables generation of PERF\_RECORD\_MMAP samples for every mmap(2) call that has PROT\_EXEC set. This allows tools to notice new executable code being mapped into a program (dynamic shared libraries for example) so that addresses can be mapped back to the original code.

comm The comm bit enables tracking of process command name as modi? fied by the exec(2) and prctl(PR\_SET\_NAME) system calls as well as writing to /proc/self/comm. If the comm\_exec flag is also successfully set (possible since Linux 3.16), then the misc flag PERF\_RECORD\_MISC\_COMM\_EXEC can be used to differentiate the exec(2) case from the others.

freq If this bit is set, then sample\_frequency not sample\_period is used when setting up the sampling interval.

This bit enables saving of event counts on context switch for inherited tasks. This is meaningful only if the inherit field

is set.

#### enable\_on\_exec

If this bit is set, a counter is automatically enabled after a call to exec(2).

task If this bit is set, then fork/exit notifications are included in the ring buffer.

### watermark

If set, have an overflow notification happen when we cross the wakeup\_watermark boundary. Otherwise, overflow notifications happen after wakeup\_events samples.

precise\_ip (since Linux 2.6.35)

This controls the amount of skid. Skid is how many instructions execute between an event of interest happening and the kernel being able to stop and record the event. Smaller skid is better and allows more accurate reporting of which events correspond to which instructions, but hardware is often limited with how small this can be.

The possible values of this field are the following:

0 SAMPLE\_IP can have arbitrary skid.

1 SAMPLE\_IP must have constant skid.

2 SAMPLE\_IP requested to have 0 skid.

3 SAMPLE\_IP must have 0 skid. See also the description of

PERF\_RECORD\_MISC\_EXACT\_IP.

mmap\_data (since Linux 2.6.36)

This is the counterpart of the mmap field. This enables genera?

tion of PERF\_RECORD\_MMAP samples for mmap(2) calls that do not

have PROT\_EXEC set (for example data and SysV shared memory).

sample\_id\_all (since Linux 2.6.38)

If set, then TID, TIME, ID, STREAM\_ID, and CPU can additionally

be included in non-PERF\_RECORD\_SAMPLEs if the corresponding sam?

ple\_type is selected.

If PERF\_SAMPLE\_IDENTIFIER is specified, then an additional ID value is included as the last value to ease parsing the record stream. This may lead to the id value appearing twice. The layout is described by this pseudo-structure:

struct sample\_id {
 { u32 pid, tid; } /\* if PERF\_SAMPLE\_TID set \*/
 { u64 time; } /\* if PERF\_SAMPLE\_TIME set \*/
 { u64 id; } /\* if PERF\_SAMPLE\_ID set \*/
 { u64 stream\_id; } /\* if PERF\_SAMPLE\_STREAM\_ID set \*/
 { u32 cpu, res; } /\* if PERF\_SAMPLE\_CPU set \*/
 { u64 id; } /\* if PERF\_SAMPLE\_IDENTIFIER set \*/
};

exclude\_host (since Linux 3.2)

When conducting measurements that include processes running VM instances (i.e., have executed a KVM\_RUN ioctl(2)), only measure events happening inside a guest instance. This is only meaning? ful outside the guests; this setting does not change counts gathered inside of a guest. Currently, this functionality is x86 only.

exclude\_guest (since Linux 3.2)

When conducting measurements that include processes running VM instances (i.e., have executed a KVM\_RUN ioctl(2)), do not mea? sure events happening inside guest instances. This is only meaningful outside the guests; this setting does not change counts gathered inside of a guest. Currently, this functional? ity is x86 only.

exclude\_callchain\_kernel (since Linux 3.7)

Do not include kernel callchains.

exclude\_callchain\_user (since Linux 3.7)

Do not include user callchains.

mmap2 (since Linux 3.16)

Generate an extended executable mmap record that contains enough

additional information to uniquely identify shared mappings.

The mmap flag must also be set for this to work.

comm\_exec (since Linux 3.16)

This is purely a feature-detection flag, it does not change ker? nel behavior. If this flag can successfully be set, then, when comm is enabled, the PERF\_RECORD\_MISC\_COMM\_EXEC flag will be set in the misc field of a comm record header if the rename event being reported was caused by a call to exec(2). This allows tools to distinguish between the various types of process renam? ing.

use\_clockid (since Linux 4.1)

This allows selecting which internal Linux clock to use when generating timestamps via the clockid field. This can make it easier to correlate perf sample times with timestamps generated by other tools.

context\_switch (since Linux 4.3)

This enables the generation of PERF\_RECORD\_SWITCH records when a context switch occurs. It also enables the generation of PERF\_RECORD\_SWITCH\_CPU\_WIDE records when sampling in CPU-wide mode. This functionality is in addition to existing tracepoint and software events for measuring context switches. The advan? tage of this method is that it will give full information even with strict perf\_event\_paranoid settings. write\_backward (since Linux 4.6) This causes the ring buffer to be written from the end to the beginning. This is to support reading from overwritable ring buffer.

namespaces (since Linux 4.11)

This enables the generation of PERF\_RECORD\_NAMESPACES records

when a task enters a new namespace. Each namespace has a combi?

nation of device and inode numbers.

ksymbol (since Linux 5.0)

This enables the generation of PERF\_RECORD\_KSYMBOL records when

new kernel symbols are registered or unregistered. This is ana?

lyzing dynamic kernel functions like eBPF.

bpf\_event (since Linux 5.0)

This enables the generation of PERF\_RECORD\_BPF\_EVENT records

when an eBPF program is loaded or unloaded.

auxevent (since Linux 5.4)

This allows normal (non-AUX) events to generate data for AUX

events if the hardware supports it.

### cgroup (since Linux 5.7)

This enables the generation of PERF\_RECORD\_CGROUP records when a new cgroup is created (and activated).

text\_poke (since Linux 5.8)

This enables the generation of PERF\_RECORD\_TEXT\_POKE records

when there's a changes to the kernel text (i.e., self-modifying

code).

wakeup\_events, wakeup\_watermark

This union sets how many samples (wakeup\_events) or bytes

(wakeup\_watermark) happen before an overflow notification hap?

pens. Which one is used is selected by the watermark bit flag.

wakeup\_events counts only PERF\_RECORD\_SAMPLE record types. To

receive overflow notification for all PERF\_RECORD types choose

watermark and set wakeup\_watermark to 1.

Prior to Linux 3.0, setting wakeup\_events to 0 resulted in no

overflow notifications; more recent kernels treat 0 the same as

1.

bp\_type (since Linux 2.6.33)

This chooses the breakpoint type. It is one of:

HW\_BREAKPOINT\_EMPTY

No breakpoint.

## HW\_BREAKPOINT\_R

Count when we read the memory location.

## HW\_BREAKPOINT\_W

Count when we write the memory location.

HW\_BREAKPOINT\_RW

Count when we read or write the memory location.

HW\_BREAKPOINT\_X

Count when we execute code at the memory location.

The values can be combined via a bitwise or, but the combination

of HW\_BREAKPOINT\_R or HW\_BREAKPOINT\_W with HW\_BREAKPOINT\_X is

not allowed.

bp\_addr (since Linux 2.6.33)

This is the address of the breakpoint. For execution break?

points, this is the memory address of the instruction of inter?

est; for read and write breakpoints, it is the memory address of

the memory location of interest.

config1 (since Linux 2.6.39)

config1 is used for setting events that need an extra register

or otherwise do not fit in the regular config field. Raw OFF?

CORE\_EVENTS on Nehalem/Westmere/SandyBridge use this field on

Linux 3.3 and later kernels.

bp\_len (since Linux 2.6.33)

bp\_len is the length of the breakpoint being measured if type is

PERF\_TYPE\_BREAKPOINT. Options are HW\_BREAKPOINT\_LEN\_1,

HW\_BREAKPOINT\_LEN\_2, HW\_BREAKPOINT\_LEN\_4, and HW\_BREAK?

POINT\_LEN\_8. For an execution breakpoint, set this to

sizeof(long).

config2 (since Linux 2.6.39)

config2 is a further extension of the config1 field.

branch\_sample\_type (since Linux 3.4)

If PERF\_SAMPLE\_BRANCH\_STACK is enabled, then this specifies what

branches to include in the branch record.

The first part of the value is the privilege level, which is a

combination of one of the values listed below. If the user does

not set privilege level explicitly, the kernel will use the

event's privilege level. Event and branch privilege levels do

not have to match.

PERF\_SAMPLE\_BRANCH\_USER

Branch target is in user space.

## PERF\_SAMPLE\_BRANCH\_KERNEL

Branch target is in kernel space.

# PERF\_SAMPLE\_BRANCH\_HV

Branch target is in hypervisor.

# PERF\_SAMPLE\_BRANCH\_PLM\_ALL

A convenience value that is the three preceding values

ORed together.

In addition to the privilege value, at least one or more of the

following bits must be set.

PERF\_SAMPLE\_BRANCH\_ANY

Any branch type.

# PERF\_SAMPLE\_BRANCH\_ANY\_CALL

Any call branch (includes direct calls, indirect calls,

and far jumps).

# PERF\_SAMPLE\_BRANCH\_IND\_CALL

Indirect calls.

```
PERF_SAMPLE_BRANCH_CALL (since Linux 4.4)
```

Direct calls.

# PERF\_SAMPLE\_BRANCH\_ANY\_RETURN

Any return branch.

PERF\_SAMPLE\_BRANCH\_IND\_JUMP (since Linux 4.2)

Indirect jumps.

PERF\_SAMPLE\_BRANCH\_COND (since Linux 3.16)

Conditional branches.

# PERF\_SAMPLE\_BRANCH\_ABORT\_TX (since Linux 3.11)

Transactional memory aborts.

PERF\_SAMPLE\_BRANCH\_IN\_TX (since Linux 3.11)

Branch in transactional memory transaction.

PERF\_SAMPLE\_BRANCH\_NO\_TX (since Linux 3.11)

Branch not in transactional memory transaction.

PERF\_SAMPLE\_BRANCH\_CALL\_STACK (since Linux 4.1) Branch is

part of a hardware-generated call stack. This requires

hardware support, currently only found on Intel x86 Haswell or newer.

sample\_regs\_user (since Linux 3.7)

This bit mask defines the set of user CPU registers to dump on

samples. The layout of the register mask is architecture-spe?

cific and is described in the kernel header file arch/ARCH/in?

clude/uapi/asm/perf\_regs.h.

sample\_stack\_user (since Linux 3.7)

This defines the size of the user stack to dump if PERF\_SAM?

PLE\_STACK\_USER is specified.

clockid (since Linux 4.1)

If use\_clockid is set, then this field selects which internal

Linux timer to use for timestamps. The available timers are de?

fined in linux/time.h, with CLOCK\_MONOTONIC, CLOCK\_MONO?

TONIC\_RAW, CLOCK\_REALTIME, CLOCK\_BOOTTIME, and CLOCK\_TAI cur?

rently supported.

aux\_watermark (since Linux 4.1)

This specifies how much data is required to trigger a

PERF\_RECORD\_AUX sample.

sample\_max\_stack (since Linux 4.8)

When sample\_type includes PERF\_SAMPLE\_CALLCHAIN, this field

specifies how many stack frames to report when generating the

callchain.

### Reading results

Once a perf\_event\_open() file descriptor has been opened, the values of the events can be read from the file descriptor. The values that are there are specified by the read\_format field in the attr structure at open time.

If you attempt to read into a buffer that is not big enough to hold the

data, the error ENOSPC results.

Here is the layout of the data returned by a read:

\* If PERF\_FORMAT\_GROUP was specified to allow reading all events in a

group at once:

```
struct read_format {
```

```
u64 nr;
                  /* The number of events */
     u64 time_enabled; /* if PERF_FORMAT_TOTAL_TIME_ENABLED */
     u64 time_running; /* if PERF_FORMAT_TOTAL_TIME_RUNNING */
     struct {
       u64 value; /* The value of the event */
                  /* if PERF_FORMAT_ID */
       u64 id;
     } values[nr];
   };
* If PERF FORMAT GROUP was not specified:
   struct read format {
     u64 value;
                   /* The value of the event */
     u64 time_enabled; /* if PERF_FORMAT_TOTAL_TIME_ENABLED */
     u64 time_running; /* if PERF_FORMAT_TOTAL_TIME_RUNNING */
     u64 id;
                  /* if PERF_FORMAT_ID */
```

```
};
```

The values read are as follows:

```
nr The number of events in this file descriptor. Available only if
```

PERF\_FORMAT\_GROUP was specified.

time\_enabled, time\_running

Total time the event was enabled and running. Normally these values are the same. Multiplexing happens if the number of events is more than the number of available PMU counter slots. In that case the events run only part of the time and the time\_enabled and time running values can be used to scale an es? timated value for the count.

value An unsigned 64-bit value containing the counter result.

id A globally unique value for this particular event; only present

if PERF\_FORMAT\_ID was specified in read\_format.

#### MMAP layout

When using perf\_event\_open() in sampled mode, asynchronous events (like

counter overflow or PROT\_EXEC mmap tracking) are logged into a ring-

buffer. This ring-buffer is created and accessed through mmap(2).

The mmap size should be 1+2<sup>n</sup> pages, where the first page is a metadata page (struct perf\_event\_mmap\_page) that contains various bits of infor? mation such as where the ring-buffer head is.

Before kernel 2.6.39, there is a bug that means you must allocate an mmap ring buffer when sampling even if you do not plan to access it. The structure of the first metadata mmap page is as follows:

```
struct perf_event_mmap_page {
```

\_\_\_u32 version; /\* version number of this structure \*/

\_\_\_u32 compat\_version; /\* lowest version this is compat with \*/

\_\_\_u32 lock; /\* seqlock for synchronization \*/

\_\_\_u32 index; /\* hardware counter identifier \*/

\_\_s64 offset; /\* add to hardware counter value \*/

\_\_\_u64 time\_enabled; /\* time event active \*/

```
___u64 time_running; /* time event on CPU */
```

union {

\_\_u64 capabilities;

```
struct {
```

\_\_u64 cap\_usr\_time / cap\_usr\_rdpmc / cap\_bit0 : 1,

```
cap_bit0_is_deprecated : 1,
cap_user_rdpmc : 1,
cap_user_time : 1,
cap_user_time_zero : 1,
```

```
};
```

```
};
```

\_\_u16 pmc\_width;

\_\_u16 time\_shift;

\_\_\_u32 time\_mult;

\_\_\_u64 time\_offset;

\_\_u64 \_\_reserved[120]; /\* Pad to 1 k \*/

- \_\_u64 data\_head; /\* head in the data section \*/
- \_\_\_u64 data\_tail; /\* user-space written tail \*/

\_\_\_u64 data\_offset; /\* where the buffer starts \*/

\_\_u64 data\_size; /\* data buffer size \*/

```
__u64 aux_head;
__u64 aux_tail;
__u64 aux_offset;
__u64 aux_size;
}
```

The following list describes the fields in the perf\_event\_mmap\_page structure in more detail:

### version

Version number of this structure.

#### compat\_version

The lowest version this is compatible with.

lock A seqlock for synchronization.

index A unique hardware counter identifier.

offset When using rdpmc for reads this offset value must be added to

the one returned by rdpmc to get the current total event count.

#### time\_enabled

Time the event was active.

#### time\_running

Time the event was running.

cap\_usr\_time / cap\_usr\_rdpmc / cap\_bit0 (since Linux 3.4)

There was a bug in the definition of cap\_usr\_time and cap\_usr\_rdpmc from Linux 3.4 until Linux 3.11. Both bits were defined to point to the same location, so it was impossible to know if cap\_usr\_time or cap\_usr\_rdpmc were actually set. Starting with Linux 3.12, these are renamed to cap\_bit0 and you should use the cap\_user\_time and cap\_user\_rdpmc fields instead.

cap\_bit0\_is\_deprecated (since Linux 3.12)

If set, this bit indicates that the kernel supports the properly separated cap\_user\_time and cap\_user\_rdpmc bits. If not-set, it indicates an older kernel where cap\_usr\_time and cap\_usr\_rdpmc map to the same bit and thus both features should be used with caution.

```
If the hardware supports user-space read of performance counters
    without syscall (this is the "rdpmc" instruction on x86), then
    the following code can be used to do a read:
      u32 seq, time_mult, time_shift, idx, width;
      u64 count, enabled, running;
      u64 cyc, time_offset;
      do {
         seq = pc->lock;
         barrier();
         enabled = pc->time_enabled;
         running = pc->time_running;
         if (pc->cap_usr_time && enabled != running) {
           cyc = rdtsc();
           time_offset = pc->time_offset;
           time_mult = pc->time_mult;
           time_shift = pc->time_shift;
         }
         idx = pc -> index;
         count = pc->offset;
         if (pc->cap_usr_rdpmc && idx) {
           width = pc->pmc_width;
           count += rdpmc(idx - 1);
         }
         barrier();
      } while (pc->lock != seq);
cap_user_time (since Linux 3.12)
    This bit indicates the hardware has a constant, nonstop time?
    stamp counter (TSC on x86).
cap_user_time_zero (since Linux 3.12)
    Indicates the presence of time_zero which allows mapping time?
```

stamp values to the hardware clock.

# pmc\_width

If cap\_usr\_rdpmc, this field provides the bit-width of the value

read using the rdpmc or equivalent instruction. This can be used to sign extend the result like:

pmc <<= 64 - pmc\_width;

pmc >>= 64 - pmc\_width; // signed shift right

count += pmc;

time\_shift, time\_mult, time\_offset

If cap\_usr\_time, these fields can be used to compute the time delta since time\_enabled (in nanoseconds) using rdtsc or simi? lar.

u64 quot, rem;

u64 delta;

quot = cyc >> time\_shift;

rem = cyc & (((u64)1 << time\_shift) - 1);

```
delta = time_offset + quot * time_mult +
```

((rem \* time\_mult) >> time\_shift);

Where time\_offset, time\_mult, time\_shift, and cyc are read in

the seqcount loop described above. This delta can then be added

to enabled and possible running (if idx), improving the scaling:

enabled += delta;

if (idx)

running += delta;

quot = count / running;

rem = count % running;

count = quot \* enabled + (rem \* enabled) / running;

### time\_zero (since Linux 3.12)

If cap\_usr\_time\_zero is set, then the hardware clock (the TSC

timestamp counter on x86) can be calculated from the time\_zero,

time\_mult, and time\_shift values:

time = timestamp - time\_zero;

quot = time / time\_mult;

rem = time % time\_mult;

cyc = (quot << time\_shift) + (rem << time\_shift) / time\_mult;

And vice versa:

```
quot = cyc >> time_shift;
```

```
rem = cyc & (((u64)1 << time_shift) - 1);
```

```
timestamp = time_zero + quot * time_mult +
```

((rem \* time\_mult) >> time\_shift);

## data\_head

This points to the head of the data section. The value continu? ously increases, it does not wrap. The value needs to be manu? ally wrapped by the size of the mmap buffer before accessing the samples.

On SMP-capable platforms, after reading the data\_head value, user space should issue an rmb().

#### data\_tail

When the mapping is PROT\_WRITE, the data\_tail value should be

written by user space to reflect the last read data. In this

case, the kernel will not overwrite unread data.

### data\_offset (since Linux 4.1)

Contains the offset of the location in the mmap buffer where

perf sample data begins.

data\_size (since Linux 4.1)

Contains the size of the perf sample region within the mmap buf? fer.

aux\_head, aux\_tail, aux\_offset, aux\_size (since Linux 4.1)

The AUX region allows mmap(2)-ing a separate sample buffer for high-bandwidth data streams (separate from the main perf sample buffer). An example of a high-bandwidth stream is instruction tracing support, as is found in newer Intel processors. To set up an AUX area, first aux\_offset needs to be set with an offset greater than data\_offset+data\_size and aux\_size needs to be set to the desired buffer size. The desired offset and size must be page aligned, and the size must be a power of two. These values are then passed to mmap in order to map the AUX buffer. Pages in the AUX buffer are included as part of the RLIMIT\_MEMLOCK resource limit (see setrlimit(2)), and also as part of the perf\_event\_mlock\_kb allowance.

By default, the AUX buffer will be truncated if it will not fit in the available space in the ring buffer. If the AUX buffer is mapped as a read only buffer, then it will operate in ring buf? fer mode where old data will be overwritten by new. In over? write mode, it might not be possible to infer where the new data began, and it is the consumer's job to disable measurement while reading to avoid possible data races.

The aux\_head and aux\_tail ring buffer pointers have the same be? havior and ordering rules as the previous described data\_head and data\_tail.

The following 2<sup>n</sup> ring-buffer pages have the layout described below. If perf\_event\_attr.sample\_id\_all is set, then all event types will have the sample\_type selected fields related to where/when (identity) an event took place (TID, TIME, ID, CPU, STREAM\_ID) described in PERF\_RECORD\_SAMPLE below, it will be stashed just after the perf\_event\_header and the fields already present for the existing fields, that is, at the end of the payload. This allows a newer perf.data file to be supported by older perf tools, with the new op? tional fields being ignored.

The mmap values start with a header:

struct perf\_event\_header {

- \_\_u32 type;
- \_\_u16 misc;
- \_\_u16 size;

```
};
```

Below, we describe the perf\_event\_header fields in more detail. For ease of reading, the fields with shorter descriptions are presented first.

size This indicates the size of the record.

misc The misc field contains additional information about the sample.

The CPU mode can be determined from this value by masking with

PERF\_RECORD\_MISC\_CPUMODE\_MASK and looking for one of the follow?

ing (note these are not bit masks, only one can be set at a time):

PERF\_RECORD\_MISC\_CPUMODE\_UNKNOWN

Unknown CPU mode.

### PERF\_RECORD\_MISC\_KERNEL

Sample happened in the kernel.

### PERF\_RECORD\_MISC\_USER

Sample happened in user code.

### PERF\_RECORD\_MISC\_HYPERVISOR

Sample happened in the hypervisor.

PERF\_RECORD\_MISC\_GUEST\_KERNEL (since Linux 2.6.35)

Sample happened in the guest kernel.

PERF\_RECORD\_MISC\_GUEST\_USER (since Linux 2.6.35)

Sample happened in guest user code.

Since the following three statuses are generated by different

record types, they alias to the same bit:

PERF\_RECORD\_MISC\_MMAP\_DATA (since Linux 3.10)

This is set when the mapping is not executable; otherwise

the mapping is executable.

PERF\_RECORD\_MISC\_COMM\_EXEC (since Linux 3.16)

This is set for a PERF\_RECORD\_COMM record on kernels more

recent than Linux 3.16 if a process name change was

caused by an exec(2) system call.

PERF\_RECORD\_MISC\_SWITCH\_OUT (since Linux 4.3)

When a PERF\_RECORD\_SWITCH or PERF\_RECORD\_SWITCH\_CPU\_WIDE

record is generated, this bit indicates that the context

switch is away from the current process (instead of into

the current process).

In addition, the following bits can be set:

#### PERF\_RECORD\_MISC\_EXACT\_IP

This indicates that the content of PERF\_SAMPLE\_IP points

to the actual instruction that triggered the event. See

also perf\_event\_attr.precise\_ip.

### PERF\_RECORD\_MISC\_EXT\_RESERVED (since Linux 2.6.35)

This indicates there is extended data available (cur? rently not used).

### PERF\_RECORD\_MISC\_PROC\_MAP\_PARSE\_TIMEOUT

This bit is not set by the kernel. It is reserved for the user-space perf utility to indicate that /proc/i[pid]/maps parsing was taking too long and was stopped, and thus the mmap records may be truncated.

type The type value is one of the below. The values in the corre? sponding record (that follows the header) depend on the type se? lected as shown.

PERF\_RECORD\_MMAP

The MMAP events record the PROT\_EXEC mappings so that we can correlate user-space IPs to code. They have the following structure:

struct {

struct perf\_event\_header header;

u32 pid, tid;

u64 addr;

u64 len;

u64 pgoff;

char filename[];

```
};
```

pid is the process ID.

tid is the thread ID.

addr is the address of the allocated memory. len is the length of the allocated memory. pgoff is the page offset of the allocated memory. filename is a string

describing the backing of the allocated memory.

#### PERF\_RECORD\_LOST

This record indicates when events are lost.

struct {

struct perf\_event\_header header;

```
u64 id;
u64 lost;
struct sample_id sample_id;
```

};

id is the unique event ID for the samples that were

lost.

lost is the number of events that were lost.

# PERF\_RECORD\_COMM

This record indicates a change in the process name.

struct {

struct perf\_event\_header header;

u32 pid;

u32 tid;

char comm[];

struct sample\_id sample\_id;

};

```
pid is the process ID.
```

```
tid is the thread ID.
```

comm is a string containing the new name of the process.

# PERF\_RECORD\_EXIT

This record indicates a process exit event.

struct {

struct perf\_event\_header header;

u32 pid, ppid;

u32 tid, ptid;

u64 time;

struct sample\_id sample\_id;

};

# PERF\_RECORD\_THROTTLE, PERF\_RECORD\_UNTHROTTLE

This record indicates a throttle/unthrottle event.

struct {

struct perf\_event\_header header;

u64 time;

u64 id;

u64 stream\_id;

struct sample\_id sample\_id;

};

## PERF\_RECORD\_FORK

This record indicates a fork event.

struct {

struct perf\_event\_header header;

u32 pid, ppid;

u32 tid, ptid;

u64 time;

struct sample\_id sample\_id;

};

# PERF\_RECORD\_READ

This record indicates a read event.

struct {

struct perf\_event\_header header;

u32 pid, tid;

struct read\_format values;

struct sample\_id sample\_id;

};

## PERF\_RECORD\_SAMPLE

This record indicates a sample.

struct {

struct perf\_event\_header header;

u64 sample\_id; /\* if PERF\_SAMPLE\_IDENTIFIER \*/

- u64 ip; /\* if PERF\_SAMPLE\_IP \*/
- u32 pid, tid; /\* if PERF\_SAMPLE\_TID \*/
- u64 time; /\* if PERF\_SAMPLE\_TIME \*/
- u64 addr; /\* if PERF\_SAMPLE\_ADDR \*/
- u64 id; /\* if PERF\_SAMPLE\_ID \*/
- u64 stream\_id; /\* if PERF\_SAMPLE\_STREAM\_ID \*/
- u32 cpu, res; /\* if PERF\_SAMPLE\_CPU \*/

u64 period; /\* if PERF\_SAMPLE\_PERIOD \*/ struct read\_format v;

/\* if PERF\_SAMPLE\_READ \*/

u64 nr; /\* if PERF\_SAMPLE\_CALLCHAIN \*/

u64 ips[nr]; /\* if PERF\_SAMPLE\_CALLCHAIN \*/

u32 size; /\* if PERF\_SAMPLE\_RAW \*/

char data[size]; /\* if PERF\_SAMPLE\_RAW \*/

u64 bnr; /\* if PERF\_SAMPLE\_BRANCH\_STACK \*/

struct perf\_branch\_entry lbr[bnr];

/\* if PERF\_SAMPLE\_BRANCH\_STACK \*/

u64 abi; /\* if PERF\_SAMPLE\_REGS\_USER \*/

u64 regs[weight(mask)];

/\* if PERF\_SAMPLE\_REGS\_USER \*/

- u64 size; /\* if PERF\_SAMPLE\_STACK\_USER \*/
- char data[size]; /\* if PERF\_SAMPLE\_STACK\_USER \*/
- u64 dyn\_size; /\* if PERF\_SAMPLE\_STACK\_USER && size != 0 \*/
- u64 weight; /\* if PERF\_SAMPLE\_WEIGHT \*/
- u64 data\_src; /\* if PERF\_SAMPLE\_DATA\_SRC \*/
- u64 transaction; /\* if PERF\_SAMPLE\_TRANSACTION \*/
- u64 abi; /\* if PERF\_SAMPLE\_REGS\_INTR \*/
- u64 regs[weight(mask)];

/\* if PERF\_SAMPLE\_REGS\_INTR \*/

- u64 phys\_addr; /\* if PERF\_SAMPLE\_PHYS\_ADDR \*/
- u64 cgroup; /\* if PERF\_SAMPLE\_CGROUP \*/

#### };

sample\_id

If PERF\_SAMPLE\_IDENTIFIER is enabled, a 64-bit unique ID is included. This is a duplication of the PERF\_SAM? PLE\_ID id value, but included at the beginning of the sample so parsers can easily obtain the value.

ip If PERF\_SAMPLE\_IP is enabled, then a 64-bit instruction

pointer value is included.

pid, tid

If PERF\_SAMPLE\_TID is enabled, then a 32-bit process ID and 32-bit thread ID are included.

#### time

If PERF\_SAMPLE\_TIME is enabled, then a 64-bit timestamp is included. This is obtained via local\_clock() which is a hardware timestamp if available and the jiffies value if not.

## addr

If PERF\_SAMPLE\_ADDR is enabled, then a 64-bit address is included. This is usually the address of a tracepoint, breakpoint, or software event; otherwise the value is 0.

id If PERF\_SAMPLE\_ID is enabled, a 64-bit unique ID is in? cluded. If the event is a member of an event group, the group leader ID is returned. This ID is the same as the one returned by PERF\_FORMAT\_ID.

stream\_id

If PERF\_SAMPLE\_STREAM\_ID is enabled, a 64-bit unique ID is included. Unlike PERF\_SAMPLE\_ID the actual ID is re? turned, not the group leader. This ID is the same as the one returned by PERF\_FORMAT\_ID.

cpu, res

If PERF\_SAMPLE\_CPU is enabled, this is a 32-bit value indicating which CPU was being used, in addition to a reserved (unused) 32-bit value.

## period

If PERF\_SAMPLE\_PERIOD is enabled, a 64-bit value indi? cating the current sampling period is written.

 v If PERF\_SAMPLE\_READ is enabled, a structure of type read\_format is included which has values for all events in the event group. The values included depend on the read\_format value used at perf\_event\_open() time. If PERF\_SAMPLE\_CALLCHAIN is enabled, then a 64-bit num? ber is included which indicates how many following 64-bit instruction pointers will follow. This is the current callchain.

## size, data[size]

If PERF\_SAMPLE\_RAW is enabled, then a 32-bit value indi? cating size is included followed by an array of 8-bit values of length size. The values are padded with 0 to have 64-bit alignment.

This RAW record data is opaque with respect to the ABI. The ABI doesn't make any promises with respect to the stability of its content, it may vary depending on event, hardware, and kernel version.

bnr, lbr[bnr]

If PERF\_SAMPLE\_BRANCH\_STACK is enabled, then a 64-bit value indicating the number of records is included, fol? lowed by bnr perf\_branch\_entry structures which each in? clude the fields:

from This indicates the source instruction (may not be

a branch).

to The branch target.

mispred

The branch target was mispredicted.

predicted

The branch target was predicted.

in\_tx (since Linux 3.11)

The branch was in a transactional memory transac?

tion.

abort (since Linux 3.11)

The branch was in an aborted transactional memory

transaction.

cycles (since Linux 4.3)

This reports the number of cycles elapsed since

the previous branch stack update.

The entries are from most to least recent, so the first

entry has the most recent branch.

Support for mispred, predicted, and cycles is optional;

if not supported, those values will be 0.

The type of branches recorded is specified by the

branch\_sample\_type field.

abi, regs[weight(mask)]

If PERF\_SAMPLE\_REGS\_USER is enabled, then the user CPU registers are recorded.

The abi field is one of PERF\_SAMPLE\_REGS\_ABI\_NONE,

PERF\_SAMPLE\_REGS\_ABI\_32, or PERF\_SAMPLE\_REGS\_ABI\_64.

The regs field is an array of the CPU registers that

were specified by the sample\_regs\_user attr field. The

number of values is the number of bits set in the sam?

ple\_regs\_user bit mask.

size, data[size], dyn\_size

If PERF\_SAMPLE\_STACK\_USER is enabled, then the user stack is recorded. This can be used to generate stack backtraces. size is the size requested by the user in sample\_stack\_user or else the maximum record size. data is the stack data (a raw dump of the memory pointed to by the stack pointer at the time of sampling). dyn\_size is the amount of data actually dumped (can be less than size). Note that dyn\_size is omitted if size is 0.

# weight

If PERF\_SAMPLE\_WEIGHT is enabled, then a 64-bit value provided by the hardware is recorded that indicates how costly the event was. This allows expensive events to stand out more clearly in profiles.

## data\_src

If PERF\_SAMPLE\_DATA\_SRC is enabled, then a 64-bit value is recorded that is made up of the following fields:

PERF_MEM_OP_NA	Not available
PERF_MEM_OP_LOAD	Load instruction
PERF_MEM_OP_STORE	Store instruction
PERF_MEM_OP_PFETCH	Prefetch
PERF_MEM_OP_EXEC	Executable code

mem\_lvl

Memory hierarchy level hit or miss, a bitwise combi?

nation of the following, shifted left by

PERF\_MEM\_LVL\_SHIFT:

PERF\_MEM\_LVL\_NA Not available

PERF\_MEM\_LVL\_HIT Hit

PERF\_MEM\_LVL\_MISS Miss

PERF\_MEM\_LVL\_L1 Level 1 cache

PERF\_MEM\_LVL\_LFB Line fill buffer

PERF\_MEM\_LVL\_L2 Level 2 cache

PERF\_MEM\_LVL\_L3 Level 3 cache

PERF\_MEM\_LVL\_LOC\_RAM Local DRAM

PERF\_MEM\_LVL\_REM\_RAM1 Remote DRAM 1 hop

PERF\_MEM\_LVL\_REM\_RAM2 Remote DRAM 2 hops

PERF\_MEM\_LVL\_REM\_CCE1 Remote cache 1 hop

PERF\_MEM\_LVL\_REM\_CCE2 Remote cache 2 hops

PERF\_MEM\_LVL\_IO I/O memory

PERF\_MEM\_LVL\_UNC Uncached memory

mem\_snoop

Snoop mode, a bitwise combination of the following,

shifted left by PERF\_MEM\_SNOOP\_SHIFT:

PERF\_MEM\_SNOOP\_NA Not available

PERF\_MEM\_SNOOP\_NONE No snoop

PERF\_MEM\_SNOOP\_HIT Snoop hit

PERF\_MEM\_SNOOP\_MISS Snoop miss

PERF\_MEM\_SNOOP\_HITM Snoop hit modified

mem\_lock

Lock instruction, a bitwise combination of the fol?

lowing, shifted left by PERF\_MEM\_LOCK\_SHIFT:

PERF\_MEM\_LOCK\_NA Not available

PERF\_MEM\_LOCK\_LOCKED Locked transaction

## mem\_dtlb

TLB access hit or miss, a bitwise combination of the

following, shifted left by PERF\_MEM\_TLB\_SHIFT:

PERF_MEM_TLB_NA	Not available
PERF_MEM_TLB_HIT	Hit
PERF_MEM_TLB_MISS	Miss
PERF_MEM_TLB_L1	Level 1 TLB
PERF_MEM_TLB_L2	Level 2 TLB
PERF_MEM_TLB_WK	Hardware walker
PERF_MEM_TLB_OS	OS fault handler

transaction

If the PERF\_SAMPLE\_TRANSACTION flag is set, then a

64-bit field is recorded describing the sources of any

transactional memory aborts.

The field is a bitwise combination of the following val?

ues:

## PERF\_TXN\_ELISION

Abort from an elision type transaction (Intel-

CPU-specific).

# PERF\_TXN\_TRANSACTION

Abort from a generic transaction.

PERF\_TXN\_SYNC

Synchronous abort (related to the reported in?

struction).

# PERF\_TXN\_ASYNC

Asynchronous abort (not related to the reported

instruction).

Retryable abort (retrying the transaction may

have succeeded).

PERF\_TXN\_CONFLICT

Abort due to memory conflicts with other threads.

PERF\_TXN\_CAPACITY\_WRITE

Abort due to write capacity overflow.

PERF\_TXN\_CAPACITY\_READ

Abort due to read capacity overflow.

In addition, a user-specified abort code can be obtained

from the high 32 bits of the field by shifting right by

PERF\_TXN\_ABORT\_SHIFT and masking with the value

PERF\_TXN\_ABORT\_MASK.

abi, regs[weight(mask)]

If PERF\_SAMPLE\_REGS\_INTR is enabled, then the user CPU registers are recorded.

The abi field is one of PERF\_SAMPLE\_REGS\_ABI\_NONE,

PERF\_SAMPLE\_REGS\_ABI\_32, or PERF\_SAMPLE\_REGS\_ABI\_64.

The regs field is an array of the CPU registers that

were specified by the sample\_regs\_intr attr field. The

number of values is the number of bits set in the sam?

ple\_regs\_intr bit mask.

phys\_addr

If the PERF\_SAMPLE\_PHYS\_ADDR flag is set, then the

64-bit physical address is recorded.

## cgroup

If the PERF\_SAMPLE\_CGROUP flag is set, then the 64-bit

cgroup ID (for the perf\_event subsystem) is recorded.

To get the pathname of the cgroup, the ID should match

to one in a PERF\_RECORD\_CGROUP .

## PERF\_RECORD\_MMAP2

This record includes extended information on mmap(2) calls returning executable mappings. The format is similar to that of the PERF\_RECORD\_MMAP record, but includes extra val?

mappings. ue

es that allow uniquely identifying shared				
st	ruct {			
	struct perf_event_header header;			
	u32	pid;		
	u32	tid;		
	u64	addr;		
	u64	len;		
	u64	pgoff;		
	u32	maj;		
	u32	min;		
	u64	ino;		
	u64	ino_generation;		
	u32	prot;		
	u32	flags;		
	char	filename[];		
	struct	sample_id sample_id;		
};				
-1	:			

```
is the process ID.
pid
```

```
is the thread ID.
tid
```

```
addr is the address of the allocated memory.
```

```
is the length of the allocated memory.
len
```

pgoff is the page offset of the allocated memory.

```
is the major ID of the underlying device.
maj
```

is the minor ID of the underlying device. min

```
is the inode number.
ino
```

```
ino_generation
```

is the inode generation.

prot is the protection information.

```
flags is the flags information.
```

# filename

is a string describing the backing of the allocated

memory.

This record reports that new data is available in the sepa?

rate AUX buffer region.

struct {

struct perf\_event\_header header;

u64 aux\_offset;

u64 aux\_size;

u64 flags;

struct sample\_id sample\_id;

};

aux\_offset

offset in the AUX mmap region where the new data be?

gins.

aux\_size

size of the data made available.

flags describes the AUX update.

# PERF\_AUX\_FLAG\_TRUNCATED

if set, then the data returned was truncated

to fit the available buffer size.

# PERF\_AUX\_FLAG\_OVERWRITE

if set, then the data returned has overwritten

previous data.

# PERF\_RECORD\_ITRACE\_START (since Linux 4.1)

This record indicates which process has initiated an in? struction trace event, allowing tools to properly correlate the instruction addresses in the AUX buffer with the proper executable.

struct {

struct perf\_event\_header header;

u32 pid;

u32 tid;

```
};
```

pid process ID of the thread starting an instruction

trace.

tid thread ID of the thread starting an instruction trace.

PERF\_RECORD\_LOST\_SAMPLES (since Linux 4.2)

When using hardware sampling (such as Intel PEBS) this record indicates some number of samples that may have been lost.

struct {

struct perf\_event\_header header;

u64 lost;

struct sample\_id sample\_id;

};

lost the number of potentially lost samples.

```
PERF_RECORD_SWITCH (since Linux 4.3)
```

This record indicates a context switch has happened. The

PERF\_RECORD\_MISC\_SWITCH\_OUT bit in the misc field indicates

whether it was a context switch into or away from the cur?

rent process.

struct {

struct perf\_event\_header header;

struct sample\_id sample\_id;

```
};
```

PERF\_RECORD\_SWITCH\_CPU\_WIDE (since Linux 4.3)

As with PERF\_RECORD\_SWITCH this record indicates a context

switch has happened, but it only occurs when sampling in

CPU-wide mode and provides additional information on the

process being switched to/from. The

PERF\_RECORD\_MISC\_SWITCH\_OUT bit in the misc field indicates

whether it was a context switch into or away from the cur?

rent process.

struct {

struct perf\_event\_header header;

u32 next\_prev\_pid;

u32 next\_prev\_tid;

struct sample\_id sample\_id;

};

next\_prev\_pid

The process ID of the previous (if switching in) or

next (if switching out) process on the CPU.

next\_prev\_tid

The thread ID of the previous (if switching in) or

next (if switching out) thread on the CPU.

PERF\_RECORD\_NAMESPACES (since Linux 4.11)

This record includes various namespace information of a

process.

struct {

struct perf\_event\_header header;

u32 pid;

u32 tid;

u64 nr\_namespaces;

struct { u64 dev, inode } [nr\_namespaces];

struct sample\_id sample\_id;

# };

pid is the process ID

```
tid is the thread ID
```

nr\_namespace

is the number of namespaces in this record

Each namespace has dev and inode fields and is recorded in

the fixed position like below:

# NET\_NS\_INDEX=0

Network namespace

```
UTS_NS_INDEX=1
```

UTS namespace

```
IPC_NS_INDEX=2
```

**IPC** namespace

PID\_NS\_INDEX=3

**PID** namespace

## USER\_NS\_INDEX=4

User namespace

# MNT\_NS\_INDEX=5

Mount namespace

# CGROUP\_NS\_INDEX=6

Cgroup namespace

# PERF\_RECORD\_KSYMBOL (since Linux 5.0)

This record indicates kernel symbol register/unregister

events.

struct {

struct perf\_event\_header header;

u64 addr;

u32 len;

- u16 ksym\_type;
- u16 flags;

char name[];

struct sample\_id sample\_id;

};

addr is the address of the kernel symbol.

len is the length of the kernel symbol.

ksym\_type

is the type of the kernel symbol. Currently the fol?

lowing types are available:

PERF\_RECORD\_KSYMBOL\_TYPE\_BPF

The kernel symbol is a BPF function.

flags If the PERF\_RECORD\_KSYMBOL\_FLAGS\_UNREGISTER is set,

then this event is for unregistering the kernel sym?

bol.

# PERF\_RECORD\_BPF\_EVENT (since Linux 5.0)

This record indicates BPF program is loaded or unloaded.

struct {

struct perf\_event\_header header;

u16 type;

u16 flags;

u32 id;

u8 tag[BPF\_TAG\_SIZE];

struct sample\_id sample\_id;

};

type is one of the following values:

# PERF\_BPF\_EVENT\_PROG\_LOAD

A BPF program is loaded

# PERF\_BPF\_EVENT\_PROG\_UNLOAD

A BPF program is unloaded

id is the ID of the BPF program.

tag is the tag of the BPF program. Currently,

BPF\_TAG\_SIZE is defined as 8.

# PERF\_RECORD\_CGROUP (since Linux 5.7)

This record indicates a new cgroup is created and activated.

struct {

struct perf\_event\_header header;

u64 id;

char path[];

struct sample\_id sample\_id;

};

id is the cgroup identifier. This can be also retrieved

by name\_to\_handle\_at(2) on the cgroup path (as a file

handle).

path is the path of the cgroup from the root.

# PERF\_RECORD\_TEXT\_POKE (since Linux 5.8)

This record indicates a change in the kernel text. This in?

cludes addition and removal of the text and the correspond?

ing length is zero in this case.

struct {

struct perf\_event\_header header;

u64 addr;

u16 old\_len;

```
u16 new_len;
u8 bytes[];
struct sample_id sample_id;
};
addr is the address of the change
```

old\_len

is the old length

new\_len

is the new length

bytes contains old bytes immediately followed by new bytes.

Overflow handling

Events can be set to notify when a threshold is crossed, indicating an overflow. Overflow conditions can be captured by monitoring the event file descriptor with poll(2), select(2), or epoll(7). Alternatively, the overflow events can be captured via sa signal handler, by enabling I/O signaling on the file descriptor; see the discussion of the F\_SE? TOWN and F\_SETSIG operations in fcntl(2). Overflows are generated only by sampling events (sample period must have a nonzero value). There are two ways to generate overflow notifications. The first is to set a wakeup\_events or wakeup\_watermark value that will trigger if a certain number of samples or bytes have been written to the mmap ring buffer. In this case, POLL\_IN is indicated. The other way is by use of the PERF\_EVENT\_IOC\_REFRESH ioctl. This ioctl adds to a counter that decrements each time the event overflows. When nonzero, POLL IN is indicated, but once the counter reaches 0 POLL\_HUP is indicated and the underlying event is disabled. Refreshing an event group leader refreshes all siblings and refreshing with a parameter of 0 currently enables infinite refreshes; these be? haviors are unsupported and should not be relied on. Starting with Linux 3.18, POLL\_HUP is indicated if the event being mon? itored is attached to a different process and that process exits.

Starting with Linux 3.4 on x86, you can use the rdpmc instruction to get low-latency reads without having to enter the kernel. Note that using rdpmc is not necessarily faster than other methods for reading event values.

Support for this can be detected with the cap\_usr\_rdpmc field in the mmap page; documentation on how to calculate event values can be found in that section.

Originally, when rdpmc support was enabled, any process (not just ones with an active perf event) could use the rdpmc instruction to access the counters. Starting with Linux 4.0, rdpmc support is only allowed if an event is currently enabled in a process's context. To restore the old behavior, write the value 2 to /sys/devices/cpu/rdpmc.

#### perf\_event ioctl calls

Various ioctls act on perf\_event\_open() file descriptors:

## PERF\_EVENT\_IOC\_ENABLE

This enables the individual event or event group specified by the file descriptor argument.

If the PERF\_IOC\_FLAG\_GROUP bit is set in the ioctl argument,

then all events in a group are enabled, even if the event speci?

fied is not the group leader (but see BUGS).

#### PERF\_EVENT\_IOC\_DISABLE

This disables the individual counter or event group specified by the file descriptor argument.

Enabling or disabling the leader of a group enables or disables the entire group; that is, while the group leader is disabled, none of the counters in the group will count. Enabling or dis? abling a member of a group other than the leader affects only that counter; disabling a non-leader stops that counter from counting but doesn't affect any other counter. If the PERF\_IOC\_FLAG\_GROUP bit is set in the ioctl argument, then all events in a group are disabled, even if the event spec? ified is not the group leader (but see BUGS).

## PERF\_EVENT\_IOC\_REFRESH

Non-inherited overflow counters can use this to enable a counter for a number of overflows specified by the argument, after which it is disabled. Subsequent calls of this ioctl add the argument value to the current count. An overflow notification with POLL\_IN set will happen on each overflow until the count reaches 0; when that happens a notification with POLL\_HUP set is sent and the event is disabled. Using an argument of 0 is considered undefined behavior.

## PERF\_EVENT\_IOC\_RESET

Reset the event count specified by the file descriptor argument to zero. This resets only the counts; there is no way to reset the multiplexing time\_enabled or time\_running values. If the PERF\_IOC\_FLAG\_GROUP bit is set in the ioctl argument, then all events in a group are reset, even if the event speci? fied is not the group leader (but see BUGS).

## PERF\_EVENT\_IOC\_PERIOD

This updates the overflow period for the event.

Since Linux 3.7 (on ARM) and Linux 3.14 (all other architec? tures), the new period takes effect immediately. On older ker? nels, the new period did not take effect until after the next overflow.

The argument is a pointer to a 64-bit value containing the de? sired new period.

Prior to Linux 2.6.36, this ioctl always failed due to a bug in the kernel.

# PERF\_EVENT\_IOC\_SET\_OUTPUT

This tells the kernel to report event notifications to the spec? ified file descriptor rather than the default one. The file de? scriptors must all be on the same CPU. The argument specifies the desired file descriptor, or -1 if

output should be ignored.

PERF\_EVENT\_IOC\_SET\_FILTER (since Linux 2.6.33)

This adds an ftrace filter to this event.

The argument is a pointer to the desired ftrace filter.

PERF\_EVENT\_IOC\_ID (since Linux 3.12)

This returns the event ID value for the given event file de? scriptor.

The argument is a pointer to a 64-bit unsigned integer to hold the result.

PERF\_EVENT\_IOC\_SET\_BPF (since Linux 4.1)

This allows attaching a Berkeley Packet Filter (BPF) program to an existing kprobe tracepoint event. You need CAP\_PERFMON (since Linux 5.8) or CAP\_SYS\_ADMIN privileges to use this ioctl. The argument is a BPF program file descriptor that was created by a previous bpf(2) system call.

PERF\_EVENT\_IOC\_PAUSE\_OUTPUT (since Linux 4.7)

This allows pausing and resuming the event's ring-buffer. A paused ring-buffer does not prevent generation of samples, but simply discards them. The discarded samples are considered lost, and cause a PERF\_RECORD\_LOST sample to be generated when possible. An overflow signal may still be triggered by the dis? carded sample even though the ring-buffer remains empty. The argument is an unsigned 32-bit integer. A nonzero value pauses the ring-buffer, while a zero value resumes the ring-buf? fer.

PERF\_EVENT\_MODIFY\_ATTRIBUTES (since Linux 4.17)

This allows modifying an existing event without the overhead of closing and reopening a new event. Currently this is supported only for breakpoint events.

The argument is a pointer to a perf\_event\_attr structure con? taining the updated event settings.

PERF\_EVENT\_IOC\_QUERY\_BPF (since Linux 4.16)

This allows querying which Berkeley Packet Filter (BPF) programs are attached to an existing kprobe tracepoint. You can only at? tach one BPF program per event, but you can have multiple events attached to a tracepoint. Querying this value on one tracepoint event returns the ID of all BPF programs in all events attached

to the tracepoint. You need CAP\_PERFMON (since Linux 5.8) or

CAP\_SYS\_ADMIN privileges to use this ioctl.

The argument is a pointer to a structure

struct perf\_event\_query\_bpf {

- \_\_u32 ids\_len;
- \_\_u32 prog\_cnt;

```
__u32 ids[0];
```

};

The ids\_len field indicates the number of ids that can fit in the provided ids array. The prog\_cnt value is filled in by the kernel with the number of attached BPF programs. The ids array is filled with the ID of each attached BPF program. If there are more programs than will fit in the array, then the kernel will return ENOSPC and ids\_len will indicate the number of pro? gram IDs that were successfully copied.

# Using prctl(2)

A process can enable or disable all currently open event groups using

the prctl(2) PR\_TASK\_PERF\_EVENTS\_ENABLE and PR\_TASK\_PERF\_EVENTS\_DISABLE

operations. This applies only to events created locally by the calling

process. This does not apply to events created by other processes at?

tached to the calling process or inherited events from a parent

process. Only group leaders are enabled and disabled, not any other

members of the groups.

# perf\_event related configuration files

# Files in /proc/sys/kernel/

# /proc/sys/kernel/perf\_event\_paranoid

The perf\_event\_paranoid file can be set to restrict access

to the performance counters.

2 allow only user-space measurements (default since Linux 4.6).

1 allow both kernel and user measurements (default before

Linux 4.6).

- allow access to CPU-specific data but not raw tracepoint samples.
- -1 no restrictions.

The existence of the perf\_event\_paranoid file is the offi? cial method for determining if a kernel supports perf\_event\_open().

## /proc/sys/kernel/perf\_event\_max\_sample\_rate

This sets the maximum sample rate. Setting this too high

can allow users to sample at a rate that impacts overall ma?

chine performance and potentially lock up the machine. The

default value is 100000 (samples per second).

## /proc/sys/kernel/perf\_event\_max\_stack

This file sets the maximum depth of stack frame entries re?

ported when generating a call trace.

## /proc/sys/kernel/perf\_event\_mlock\_kb

Maximum number of pages an unprivileged user can mlock(2).

The default is 516 (kB).

#### Files in /sys/bus/event\_source/devices/

Since Linux 2.6.34, the kernel supports having multiple PMUs avail?

able for monitoring. Information on how to program these PMUs can

be found under /sys/bus/event\_source/devices/. Each subdirectory

corresponds to a different PMU.

/sys/bus/event\_source/devices/\*/type (since Linux 2.6.38)

This contains an integer that can be used in the type field of perf\_event\_attr to indicate that you wish to use this PMU.

/sys/bus/event\_source/devices/cpu/rdpmc (since Linux 3.4)
If this file is 1, then direct user-space access to the per?
formance counter registers is allowed via the rdpmc instruc?
tion. This can be disabled by echoing 0 to the file.
As of Linux 4.0 the behavior has changed, so that 1 now
means only allow access to processes with active perf
events, with 2 indicating the old allow-anyone-access behav?

ior.

/sys/bus/event\_source/devices/\*/format/ (since Linux 3.4)
This subdirectory contains information on the architecturespecific subfields available for programming the various
config fields in the perf\_event\_attr struct.
The content of each file is the name of the config field,
followed by a colon, followed by a series of integer bit
ranges separated by commas. For example, the file event may
contain the value config1:1,6-10,44 which indicates that
event is an attribute that occupies bits 1,6?10, and 44 of
perf\_event\_attr::config1.

/sys/bus/event\_source/devices/\*/events/ (since Linux 3.4)

This subdirectory contains files with predefined events. The contents are strings describing the event settings ex? pressed in terms of the fields found in the previously men? tioned ./format/ directory. These are not necessarily com? plete lists of all events supported by a PMU, but usually a subset of events deemed useful or interesting. The content of each file is a list of attribute names sepa? rated by commas. Each entry has an optional value (either hex or decimal). If no value is specified, then it is as? sumed to be a single-bit field with a value of 1. An exam? ple entry may look like this: event=0x2,inv,ldlat=3.

#### /sys/bus/event\_source/devices/\*/uevent

This file is the standard kernel device interface for in? jecting hotplug events.

/sys/bus/event\_source/devices/\*/cpumask (since Linux 3.7)

The cpumask file contains a comma-separated list of integers that indicate a representative CPU number for each socket (package) on the motherboard. This is needed when setting up uncore or northbridge events, as those PMUs present socket-wide events. perf\_event\_open() returns the new file descriptor, or -1 if an error occurred (in which case, errno is set appropriately).

## ERRORS

The errors returned by perf\_event\_open() can be inconsistent, and may vary across processor architectures and performance monitoring units. E2BIG Returned if the perf\_event\_attr size value is too small (smaller than PERF\_ATTR\_SIZE\_VER0), too big (larger than the page size), or larger than the kernel supports and the extra bytes are not zero. When E2BIG is returned, the perf\_event\_attr size field is overwritten by the kernel to be the size of the structure it was expecting.

- EACCES Returned when the requested event requires CAP\_PERFMON (since Linux 5.8) or CAP\_SYS\_ADMIN permissions (or a more permissive perf\_event paranoid setting). Some common cases where an un? privileged process may encounter this error: attaching to a process owned by a different user; monitoring all processes on a given CPU (i.e., specifying the pid argument as -1); and not setting exclude\_kernel when the paranoid setting requires it.
- EBADF Returned if the group\_fd file descriptor is not valid, or, if PERF\_FLAG\_PID\_CGROUP is set, the cgroup file descriptor in pid is not valid.

EBUSY (since Linux 4.1)

Returned if another event already has exclusive access to the PMU.

EFAULT Returned if the attr pointer points at an invalid memory ad? dress.

EINVAL Returned if the specified event is invalid. There are many pos? sible reasons for this. A not-exhaustive list: sample\_freq is higher than the maximum setting; the cpu to monitor does not ex? ist; read\_format is out of range; sample\_type is out of range; the flags value is out of range; exclusive or pinned set and the event is not a group leader; the event config values are out of range or set reserved bits; the generic event selected is not supported; or there is not enough room to add the selected event.

- EINTR Returned when trying to mix perf and ftrace handling for a up? robe.
- EMFILE Each opened event uses one file descriptor. If a large number of events are opened, the per-process limit on the number of open file descriptors will be reached, and no more events can be created.
- ENODEV Returned when the event involves a feature not supported by the current CPU.
- ENOENT Returned if the type setting is not valid. This error is also returned for some unsupported generic events.

ENOSPC Prior to Linux 3.3, if there was not enough room for the event, ENOSPC was returned. In Linux 3.3, this was changed to EINVAL. ENOSPC is still returned if you try to add more breakpoint

events than supported by the hardware.

ENOSYS Returned if PERF\_SAMPLE\_STACK\_USER is set in sample\_type and it

is not supported by hardware.

# EOPNOTSUPP

Returned if an event requiring a specific hardware feature is requested but there is no hardware support. This includes re? questing low-skid events if not supported, branch tracing if it is not available, sampling if no PMU interrupt is available, and branch stacks for software events.

## EOVERFLOW (since Linux 4.8)

Returned if PERF\_SAMPLE\_CALLCHAIN is requested and sam? ple\_max\_stack is larger than the maximum specified in /proc/sys/kernel/perf\_event\_max\_stack.

# EPERM Returned on many (but not all) architectures when an unsupported exclude\_hv, exclude\_idle, exclude\_user, or exclude\_kernel set? ting is specified.

It can also happen, as with EACCES, when the requested event re?

quires CAP\_PERFMON (since Linux 5.8) or CAP\_SYS\_ADMIN permis?

sions (or a more permissive perf\_event paranoid setting). This

includes setting a breakpoint on a kernel address, and (since

Linux 3.13) setting a kernel function-trace tracepoint.

ESRCH Returned if attempting to attach to a process that does not ex? ist.

## VERSION

perf\_event\_open() was introduced in Linux 2.6.31 but was called perf\_counter\_open(). It was renamed in Linux 2.6.32.

## CONFORMING TO

This perf\_event\_open() system call Linux-specific and should not be used in programs intended to be portable.

## NOTES

Glibc does not provide a wrapper for this system call; call it using

syscall(2). See the example below.

The official way of knowing if perf\_event\_open() support is enabled is checking for the existence of the file /proc/sys/ker? nel/perf\_event\_paranoid.

CAP\_PERFMON capability (since Linux 5.8) provides secure approach to performance monitoring and observability operations in a system accord? ing to the principal of least privilege (POSIX IEEE 1003.1e). Access? ing system performance monitoring and observability operations using CAP\_PERFMON rather than the much more powerful CAP\_SYS\_ADMIN excludes chances to misuse credentials and makes operations more secure. CAP\_SYS\_ADMIN usage for secure system performance monitoring and ob? servability is discouraged in favor of the CAP\_PERFMON capability.

# BUGS

The F\_SETOWN\_EX option to fcntl(2) is needed to properly get overflow signals in threads. This was introduced in Linux 2.6.32. Prior to Linux 2.6.33 (at least for x86), the kernel did not check if events could be scheduled together until read time. The same happens on all known kernels if the NMI watchdog is enabled. This means to see if a given set of events works you have to perf\_event\_open(), start, then read before you know for sure you can get valid measurements.

Prior to Linux 2.6.34, event constraints were not enforced by the ker? nel. In that case, some events would silently return "0" if the kernel scheduled them in an improper counter slot. Prior to Linux 2.6.34, there was a bug when multiplexing where the wrong results could be returned. Kernels from Linux 2.6.35 to Linux 2.6.39 can quickly crash the kernel if "inherit" is enabled and many threads are started. Prior to Linux 2.6.35, PERF\_FORMAT\_GROUP did not work with attached processes. There is a bug in the kernel code between Linux 2.6.36 and Linux 3.0 that ignores the "watermark" field and acts as if a wakeup\_event was chosen if the union has a nonzero value in it. From Linux 2.6.31 to Linux 3.4, the PERF\_IOC\_FLAG\_GROUP ioctl argument was broken and would repeatedly operate on the event specified rather than iterating across all sibling events in a group. From Linux 3.4 to Linux 3.11, the mmap cap\_usr\_rdpmc and cap\_usr\_time bits mapped to the same location. Code should migrate to the new cap user rdpmc and cap user time fields instead. Always double-check your results! Various generalized events have had wrong values. For example, retired branches measured the wrong thing on AMD machines until Linux 2.6.35.

## EXAMPLES

The following is a short example that measures the total instruction count of a call to printf(3). #include <stdlib.h> #include <stdlib.h> #include <stdlib.h> #include <unistd.h> #include <string.h> #include <string.h> #include <sys/ioctl.h> #include <asm/unistd.h> static long perf\_event\_open(struct perf\_event\_attr \*hw\_event, pid\_t pid,

```
int cpu, int group_fd, unsigned long flags)
```

```
{
```

```
int ret;
```

```
ret = syscall(__NR_perf_event_open, hw_event, pid, cpu,
```

```
group_fd, flags);
```

```
return ret;
```

```
}
```

```
int
```

```
main(int argc, char **argv)
```

```
{
```

```
struct perf_event_attr pe;
```

long long count;

int fd;

```
memset(&pe, 0, sizeof(pe));
```

```
pe.type = PERF_TYPE_HARDWARE;
```

```
pe.size = sizeof(pe);
```

```
pe.config = PERF_COUNT_HW_INSTRUCTIONS;
```

```
pe.disabled = 1;
```

```
pe.exclude_kernel = 1;
```

```
pe.exclude_hv = 1;
```

```
fd = perf_event_open(&pe, 0, -1, -1, 0);
```

if (fd == -1) {

fprintf(stderr, "Error opening leader %llx\n", pe.config);

```
exit(EXIT_FAILURE);
```

```
}
```

ioctl(fd, PERF\_EVENT\_IOC\_RESET, 0); ioctl(fd, PERF\_EVENT\_IOC\_ENABLE, 0); printf("Measuring instruction count for this printf\n"); ioctl(fd, PERF\_EVENT\_IOC\_DISABLE, 0); read(fd, &count, sizeof(count)); printf("Used %Ild instructions\n", count); close(fd);

# SEE ALSO

perf(1), fcntl(2), mmap(2), open(2), prctl(2), read(2)

Documentation/admin-guide/perf-security.rst in the kernel source tree

# COLOPHON

This page is part of release 5.10 of the Linux man-pages project. A description of the project, information about reporting bugs, and the latest version of this page, can be found at https://www.kernel.org/doc/man-pages/.

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